Docket No.

249083US2S/ams

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

Daisaburo TAKASHIMA

SERIAL NO:

10/780,590

GAU:

FILED:

February 19, 2004

EXAMINER:

FOR:

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

SIR

Applicant(s) wish to disclose the following information.

REFERENCES

- The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

- Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s), together with a copy of the claims and drawings of the pending application(s) is attached along with PTO 1449.
- A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

- ☐ Each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- □ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

Please charge any additional fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted.

OBLON, SPIVAK, McCLELLAND,

MAIER & NEUSTADT, P.C.

Marvin J. Spivak

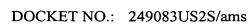
Registration No. 24,913

Joseph A. Scafetta, Jr. Registration No. 26, 803

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STATEMENT OF RELEVANCY

Reference AO (JP 10-255483) on Form PTO-1449:

This patent is the same as Fig. 50 of the submitted application. Speed of this patent is slower than the speed in the submitted application due to serial cell transistors.

Reference AP (JP 11-177036) on Form PTO-1449:

This patent is also the same as Fig. 50 of the submitted application. Speed of this patent is also slower than the speed in the submitted application.

Reference AQ (JP 2000-22010) on Form PTO-1449:

This patent enables a larger signal than the conventional prior art. But speed of this patent is also slower than the speed in the submitted application.

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Form PTO 1449 (Modified)			ATTY DOCKET NO. 249083US2S	FATS TRADE	MARK	SERIAL NO. 10/780,590			
				APPLICANT					
LIST OF	REFE	RENCES CITED BY AP	PLICANT	Daisaburo TAKASHIMA					
				FILING DATE			GROUP		
				February 19, 2004					
U.S. PATENT DOCUMENTS									
EXAMINER		DOCUMENT	DATE			000	SUB	FILING DATE	
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FOREIGN PATENT DOCUMENTS									
		DOCUMENT NUMBER	DATE	COUNTRY			TRANSLATION YES NO		
	AO	10-255483	09/25/1998	JAPAN				X	
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OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)									
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Examiner						Date Considered			
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.									